

### REMARKS

The Office Action dated February 6, 2008 has been carefully considered. Claims 1, 8 and 10 were amended. Reconsideration of the application is respectfully requested.

#### ***Interview Summary***

Applicants wish to express their gratitude to Examiner Dinh for the courtesy of April 23, 2008 interview with the applicant's representative. During the interview, the rejections of the pending claims over the cited prior art were discussed. Following is the recitation of the arguments advanced by the applicants' representative during the interview.

#### ***Claim Rejections Under 35 U.S.C. § 102***

In the Office Action, claims 1-3 and 8-11 were rejected under 35 U.S.C. § 102(b) as being allegedly anticipated by U.S. Patent No. 6,049,476 to Laudon et al. Applicants traverse.

According to the M.P.E.P., a claim is anticipated under 35 U.S.C. § 102(b) only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference.<sup>1</sup>

Applicants submit that Laudon fails to anticipate claims 1-3 and 8-11 at least because it does not disclose a memory module having, *inter alia*, “a plurality of different types of memory devices mounted on said printed circuit board, wherein said plurality of memory devices have different memory architectures” as recited in amended claims 1, 8 and 10.

More specifically, the present application teaches a multifunction memory system. The system includes a memory module including a printed circuit board and a plurality of different types of memory devices mounted on the board. Each memory device may have different

memory architecture based on its memory technology. Examples of memory technologies include, but are not limited to, DDR SDRAM, FCRAM and RDRAM. See, e.g., paragraph 26.

In contrast, Laudon discloses a dual in-line memory module (DIMM) including a circuit board, and first and second memories mounted on the circuit board. The first memory is configured for storing data and is also called a data memory. The second memory, called a state memory, is configured for storing state information corresponding to at least a portion of the data stored in the data memory. See, e.g., col. 2, ll. 30-36. In other words, the first and second memories may store different types of information. Laudon also teaches that the sized of the first and second memory may be different. See, e.g., col. 3, ll. 35-37 and 43-44.

Laudon however does not disclose, teach or even suggest that the first and second memories may have different architectures, as disclosed in the present application. In particular, Laudon discloses that both the first and second memories include SDRAM memory modules. See, e.g., col. 2, ll. 62-64; col. 3, ll. 5-12, 18-21, 23, 35-37 and 43-44; col. 5, ll. 14 and 16. In other words the first and second memory modules have the same architecture, namely SDRAM. In fact, there is no even a mention that the first and second memories may be anything else but SDRAMs. Moreover, not only Laudon fails to disclose that a single DIMM may include memory modules having different architectures, as recited in the present application, it also fails to disclose that different DIMMs may include memory modules having different architectures.

In view of the above, independent claims 1, 8 and 10 as well as all claims dependent thereon are patentable over Laudon.

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<sup>1</sup> Manual of Patent Examining Procedure (MPEP) § 2131.

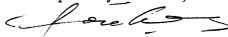
***Claim Rejections Under 35 U.S.C. §103***

In the Office Action, claims 4-7 were rejected under 35 U.S.C. § 103(a) as being allegedly unpatentable over Laudon in view of U.S. Patent No. 6,115,278 to Deneroff et al. (Deneroff). Applicants submit that claims 4-7 are patentable over Laudon for the reasons set forth above and those advanced in the applicants' November 12, 2007 Response.

***Conclusion***

In view of the above, applicants submit that the present application is in condition for allowance. Favorable disposition to that effect is respectfully requested. If, in the opinion of the Examiner, an interview would expedite the prosecution of this application, the Examiner is invited to call the undersigned attorney of record at the number indicated below.

Respectfully submitted,



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